

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

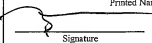
In re Applicant:
Steven J. Sistare, et al

Patent No.: 7,424,712
Issued: September 9, 2008

Serial No.: 09/303,464
Filed: April 30, 1999

For: System and Method for Controlling
Co-Scheduling of Processes of
Parallel Program

Atty. Docket No.: 5181-93900

****CERTIFICATE OF E-FILED TRANSMISSION**** I hereby certify that this correspondence is being transmitted via electronic filing to the United States Patent and Trademark Office on the date shown below B. Noël Kivlin Printed Name  Signature 10/20/08 Date
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REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith for filing in the captioned case is the Form PTO-1050. Errors which occur at important points in the captioned patent or which may otherwise affect the understanding or interpretation of the patent are thereon corrected.

All of the errors shown in PTO-1050 are due to Patent Office oversights. In particular, in this application, the Patent Office erroneously issued the patent without Applicant's submittal of Form PTOL-84. (The issue fee of \$1,400.00 was charged to the deposit account listed below.) A Certificate of Correction is requested under 35 U.S.C. § 254. Applicants believe that no fees are required, however, should any fees be required, please deduct them from Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505/5181-93900.

Respectfully submitted,


B. Noël Kivlin
Attorney for Applicant
Reg. No. 33,929

Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.
P.O. Box 398
Austin, Texas 78767-0398
(512) 853-8840
Date: 10/20/08

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,424,712

DATED : September 9, 2008

INVENTOR(S) : Steven J. Sistare, Nicholas J. Nevin, Anthony L. Kimball

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Assignee:

The Assignee is Sun Microsystems, Inc.

MAILING ADDRESS OF SENDER:

B. Noel Kivlin, Esq.,
MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C.
P.O. Box 398
Austin, Texas 78767-0398
5181-93900

PATENT NO. 7,424,712

Certificate of Correction (PTO Form 1050)